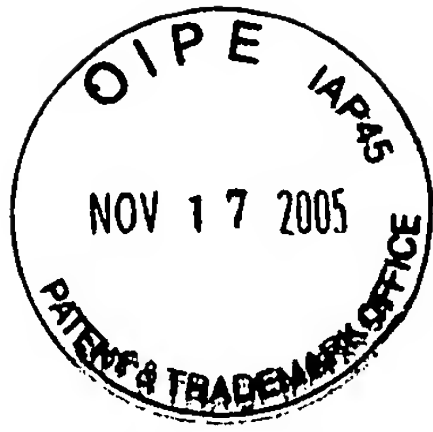


00862.023135



G/C

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent of:

Kotaro AKUTSU et al.

Application No.: 10/617,682

Filed: July 14, 2003

For: ELECTRON BEAM EXPOSURE APPARATUS AND  
SEMICONDUCTOR DEVICE MANUFACTURING  
METHOD

U.S. Patent No. 6,930,756 B2

Issued: August 16, 2005

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) Examiner: D. J. Rutledge  
:  
) Group Art Unit: 2851  
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:  
) November 17, 2005  
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)

**Certificate**  
NOV 21 2005  
**of Correction**

The Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450  
**Attention: Certificate of Correction Branch**

REQUEST FOR  
CERTIFICATE OF CORRECTION  
UNDER RULE 322

Sir:

Patentees request that a Certificate of Correction be issued by the Patent and  
Trademark Office due to errors which appear in the printed patent as a result of U.S. Patent and  
Trademark Office mistakes.

No fee is believed to be due with the filing of this paper. Nevertheless, the Commissioner may charge Deposit Account No. 06-1205 should any fee be deemed necessary for filing this paper.

Patentees' undersigned attorney may be reached in our Washington, D.C. Office by telephone at (202) 530-1010. All correspondence should continue to be directed to our below-listed address.

Respectfully submitted,

A handwritten signature in black ink, appearing to read "Steven E. Warner", is written over a horizontal line.

Attorney for Patentees  
Steven E. Warner  
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UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,930,756 B2

DATED : August 16, 2005

INVENTOR(S): Kotaro AKUTSU et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

COLUMN 2:

Line 37, "electron-beams" should read -- electron beams --.

COLUMN 10:

Line 3, "process (chip" should read -- process (dicing and bonding) and packing process (chip --.

MAILING ADDRESS OF SENDER:

PATENT NO. 6,930,756 B2

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